

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. SAITO et al
Serial No.: (New Continuation application under 37 CFR §1.53[b] of
U.S. Application Serial No. 09/613,138)
Filed: March 11, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND FABRICATION PROCESS THEREOF
Group: 2814
Examiner: P. CAO

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & §1.98

MS: PATENT APPLICATIONS

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

March 11, 2004

Sir:

In the matter of the above-identified application, applicants are attaching hereto, in a form equivalent to Form PTO-1449, a listing of all the art documents of record (cited by the Examiner/submitted by applicants) in the chain of continuing applications (as listed on page 1 of the Specification), pursuant to the duty of disclosure/candor requirements.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified continuation application and, therefore, this IDS meets the requirements pursuant to 37 CFR §1.97(b).

As to the requirement of 37 CFR §1.98(a)(3) for any concise explanation of relevance to the extent that any of the listed documents is not in the English language, this is met by: the discussion thereof in the Specification, the

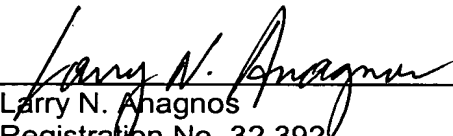
submission of any English language translation, and the citation by the Examiner in the prior application(s).

It is understood that since copies of the listed documents should be available in the file wrapper of the prior applications, copies thereof are not included herewith, consistent with 37 CFR §1.98(d) and pursuant to the guidelines set forth in MPEP §609.

It is submitted this IDS is in compliance with the rules of practice as well as with USPTO guidelines. Therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgment of entry as well as of formal consideration thereof by the Examiner is respectfully requested.

If any costs are due in accordance with the filing of this Information Disclosure Statement, please charge same to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (501.36455CC2).

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP


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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known			
		Application Number	R53(b) CONT. of S.N. 09/613,138		
		Filing Date	March 11, 2004		
		First Named Inventor	T. SAITO et al		
		Art Unit	2814		
Sheet	1	of	2	Examiner Name	P. CAO
				Attorney Docket Number	501.36455CC2

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		US- 4.107.726		08-15-1978	Schilling	
		US- 4.907.066		03-06-1990	Thomas et al	
		US- 4.984.061		01-08-1991	H. Matsumoto	
		US- 5.394.013		02-28-1995	Oku et al	
		US- 5.463.255		10-31-1995	T. Isono	
		US- 5.565.707		10-15-1996	Colgan et al	
		US- 5.659.201		08-19-1997	Wollesen	
		US- 5.691.572		11-25-1997	Chung	
		US- 5.739.579		04-14-1998	Chiang et al	
		US- 5.786.637		07-28-1998	Tabara	
		US- 5.856.707		01-05-1999	Sardella	
		US- 6.181.012		01-30-2001	Edelstein et al	
		US- 6.188.135		02-13-2001	Chan et al	
		US- 6.265.779		07-24-2001	Grill et al	
		US- 6.291.885		09-18-2001	Cabral, Jr., et al	
		US-				

FOREIGN PATENT DOCUMENTS						
Examiner 'Initials'	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
		JP-A-8-78410	3/22/1996			AB
		JP-A-8-148560	06/07/1996			AB

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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				Application Number	R53(b) CONT. of S.N. 09/613,138
				Filing Date	March 11, 2004
				First Named Inventor	T. SAITO et al
				Group Art Unit	2814
				Examiner Name	P. CAO
Sheet	2	of	2	Attorney Docket Number	501.36455CC2

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		MAEDA, K., "Semiconductor Manufacturing Equipment Corresponding to the Introduction of New Processes and Materials," Electron Materials, March 1996, pp. 22-27.	
		BAI et al, "Copper Interconnection Deposition Techniques and Integration," 1996 Symposium on VLSI Technology Digest of Technical Papers, pp. 48-49.	
		LICATA et al., "Interconnect Fabrication Processes and the Development of Low-Cost Wiring for CMOS Products," IBM J. Res. Develop., Vol. 39, No. 4, July 1995, pp. 419-435.	

Examiner Signature		Date Considered	
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